



# WR04X

±1%, ±5%

# General purpose chip resistors

Size 0402

Customer	:
Approval No	:
Issue Date	:

Customer Approval :



# FEATURE

- 1. Small size and light weight
- 2. High reliability and stability
- 3. Reduced size of final equipment
- 4. Lower assembly costs
- 5. Higher component and equipment reliability
- 6. Lead free product is available

# **APPLICATION**

- Mobile phone
- PDA
- Camcorders
- Pagers
- Palmtop computers

# DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Lead-tin or Tin (Lead free) alloy.

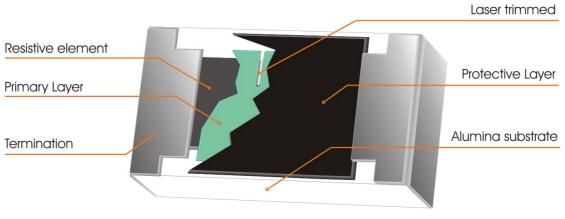


Fig 1. Consctruction of Chip-R WR04

# QUICK REFERENCE DATA

Item	General Sp	pecification
Series No.	WR	04X
Size code	0402 (	1005)
Resistance Tolerance	±5% (E24 series)	$\pm$ 1% (E96 series),
Resistance Range	1Ω ~ 10MΩ ( ±5% ), Jumper (0Ω)	10Ω ~ 1MΩ ( ±1% )
TCR (ppm/°C)		
$R \ge 1 M \Omega$	-300ppm/°C ~ +500 ppm/°C	
1MΩ > R ≥10Ω	$\leq$ ± 200 ppm/°C	$\leq$ ± 200 ppm/°C
R < 10Ω	-300ppm/°C ~ +500 ppm/°C	
Max. dissipation at T <sub>amb</sub> =70°C	1/16 W	
Max. Operation Voltage (DC or RMS)	50V	
Max. Overload Voltage (DC or RMS)	100V	
Climatic category (IEC 60068)	55/15	55/56

Note :

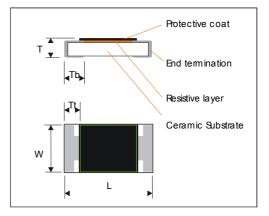
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

RCWV =  $\sqrt{\text{Rated Power} \times \text{Resistance Value}}$  or Max. RCWV listed above, whichever is lower.

3. For Resistance range  $1\Omega \sim 10\Omega$  and  $1M\Omega \sim 10M\Omega$  of tolerance  $\pm 1\%$  series No. is WR04W, please refer to specification respectively.

**Dimensions:** 

	WR04X		
L	$1.00\pm0.05$		
w	$0.50\pm0.05$		
Т	$0.35\pm0.05$		
Tb	$0.25\pm0.10$		
Tt	$0.20\pm0.10$		



### MARKING

WR04 has no marking on the product overcoat for both 5% & 1%.

<sup>1.</sup> This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"



# FUNCTIONAL DESCRIPTION

#### Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of  $\pm$ 5%, and E96 series for resistors with a tolerance of  $\pm$ 1%. The values of the E24/E96 series are in accordance with "IEC publication 60063"

# Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

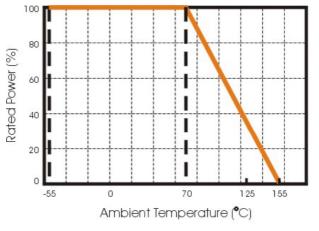


Figure 2. Maximum dissipation in percentage of rated power As a function of the ambient temperature

# CATALOGUE NUMBERS

The resistors have a catalogue number starting with :

WR04	X	3742	J	т	_
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WR04 : 0402	X : Normal W : Out of resistance range (1% of 1Ω ~ 10Ω, 1ΜΩ~ 10ΜΩ)	E24 : 2 significant digits followed by no. of zeros and a blank $4.7\Omega = 4R7$ $10\Omega = 100$ $220\Omega = 221$ Jumper = 000 ("_" means a blank) E96 : 3 significant digits followed by no. of zeros $102\Omega = 1020$ $37.4K\Omega = 3742$		T : 7" Reeled taping B : Bulk	_ = SnPb base ("_" means a blank) L = Sn base (lead free)

1. Reeled tape packaging : 8mm width paper taping 10,000pcs per reel.

2. Bulk packaging : 10,000pcs per poly-bag

#### Walsin Technology Corporation



#### MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

# SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for one minute. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs). Surface Mount Resistors are tested for solderability at 230°C during 2 seconds The

Surface Mount Resistors are tested for solderability at 230°C during 2 seconds. The test condition for no leaching is 260°C for 60 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

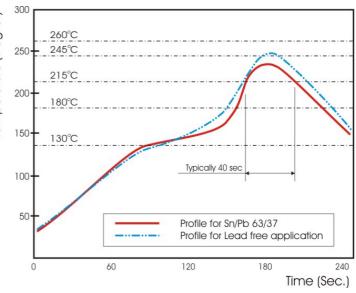


Fig 3. Infrared soldering profile for Chip Resistors WR04X

### TEST CONDITION FOR JUMPER (0 Ω)

Item	WR04X
Power Rating At 70°C	1/16W
Resistance	MAX. 50mΩ
Rated Current	1A
Peak Current	1.5A
Operating Temperature	-55~155°C



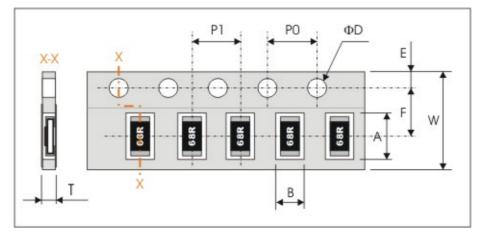
# TEST AND REQUIREMENTS

TEST	PROCEDURE	REQUIREMENT		
IESI	PROCEDURE	Resistor	Jumper	
DC resistance	DC resistance values measured at the test voltages specified below :	Within the specified tolerance	< 50mΩ	
	<10Ω@0.1V, <100Ω@0.3V, <1KΩ@1.0V,			
	<10KΩ@3V, <100KΩ@10V, <1MΩ@25V,			
	<10MΩ@30V			
Temperature Coefficient of	Natural resistance change per change in degree centigrade.	Test temperature –55~+155°C	N/a	
Resistance ( TCR )	$R_2 - R_1$	≥1MΩ, -300~+500ppm/°C		
(101()	$\left  \frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} \right $	≥10Ω, ≤±200ppm/°C		
		<10Ω -300~+500ppm/°C		
	R <sub>1</sub> : Resistance at reference temperature			
	R <sub>2</sub> : Resistance at test temperature			
	t <sub>1</sub> :25°C			
Short time overload ( STOL )	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	$\Delta$ R/R max. $\pm$ (2%+0.10 $\Omega$ )	< 50mΩ	
Resistance to	Unmounted chips $10\pm1$ seconds, $270\pm5^{\circ}C$	no visible damage	no visible	
soldering heat		$\Delta$ R/R max. $\pm(1\%+0.10\Omega)$	damage,	
			< 50mΩ	
Solderability	Termination SnPb base : Unmounted chips completely immersed for $2\pm0.5$ sec. in a solder	good tinning (>95% covered)		
	bath at $230\pm5^{\circ}$ C	no visible damage		
	Termination Sn base (lead free) : Unmounted chip completely immersed in a lead free solder bath, 245°C±5°C, 3±1 sec			
Temperature	1. 30 minutes at -55°C±3°C,	no visible damage	no visible	
cycling	2. 2~3 minutes at room temperature,	$\Delta$ R/R max. ±(1%+0.10 $\Omega$ )	damage,	
	3. 30 minutes at +155°±3°C,		< 50mΩ	
	4. 2~3 minutes at room temperature,			
	Total 5 continuous cycles			
Load life	$70\pm2°C$ , 1000 hours, loaded with RCWV or Vmax,1.5 hours on and 0.5 hours off	10Ω~1MΩ ±(3%+0.1Ω)	< 50mΩ	
(endurance)		<10Ω or ≥1MΩ ±(5%+0.1Ω)		
Load life in	1000 hours, at rated continuous working voltage in	10Ω~1MΩ ±(3%+0.1Ω)	< 50mΩ	
Humidity	humidity chamber controller at $40^{\circ}C\pm 2^{\circ}C$ and $90\sim95\%$ relative humidity, 1.5hours on and 0.5 hours off	<10Ω or ≥1MΩ ±(5%+0.1Ω)		
Bending and Termination	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 5 mm, once for 10seconds	no visible damage	no visible damage,	
strength	Pulling test : 500grams	$\Delta$ R/R max. ±(1%+0.10 $\Omega$ )	< 50mΩ	
			. 001122	



# PACKAGING

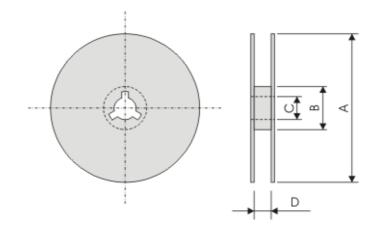
Paper Tape specifications (unit :mm)



Series No.	A	В	W	F	E
WR04X	$1.20 \pm 0.10$	0.7±0.10	8.00±0.20	$3.50 \pm 0.05$	1.75±0.10

Series No.	P1	P0	ΦD	Т
WR04X	2.00±0.10	4.00±0.10	$\Phi 1.50^{+0.1}_{-0.0}$	0.40±0.05

#### **Reel dimensions**



Symbol	А	В	С	D
(unit : mm)	$\Phi$ 178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5

# Taping quantity and Tape material

- Chip resistors 10,000 pcs/reel, Paper tape.